

Searching for **reduce cache thrashing and profiling**.

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[Data Units: A Process Interaction Paradigm - William Delaney \(1991\)](#) (Correct)
cloak of a classical procedure call paradigm, RPC **reduces** programming complexity in distributed systems. difficulties result from two phenomena: **thrashing** and false sharing. **Thrashing** occurs when two
www.cse.nd.edu/pub/Reports/Pre-1992/tr913.ps.gz

[Optimally Profiling and Tracing Programs - Ball, Larus \(1992\)](#) (Correct) (103 citations)
and trace programs. These algorithms greatly **reduce** the cost of measuring programs. **Profiling**, which utility cord, which reorganizes blocks to improve **cache** behavior, or interprocedural delay slot filling.
[Optimally Profiling And Tracing Programs Thomas Ball James R.](#)
www.deas.harvard.edu/cs/academics/courses/cs248r/readings/qpt-opt-prof-trace.ps.gz

[Design and Performance Evaluation of a Cache Assist to implement.. - John \(1997\)](#) (Correct) (5 citations)
the **cache's** effectiveness on many references is **reduced**. This paper presents a selective caching scheme
[Design and Performance Evaluation of a Cache Assist to implement Selective Caching L. John](#)
www.ece.utexas.edu/~ljohn/annex.ps

[On Performance of Caching Proxies - Rousskov](#) (Correct) (41 citations)
network bandwidth on outbound connections and may **reduce** client response time. Local copies of the objects and conducted a series of experiments on large Web **caches**. We have discovered many interesting and performance analysis, Web caching, caching proxy, **profiling**, Squid. ii 1 Introduction The World Wide Web
www.cs.ndsu.nodak.edu/~rousskov/research/cache/squid/profiling/papers/on.performance.ps.gz

[Caching in on Sisal: Cache Performance of Sisal vs. Fortran - Nico Park](#) (Correct)
themselves. We find that not only does this **reduce** the clutter of the graphs, but it also shows what
[Caching in on Sisal: Cache Performance of Sisal vs. Fortran P. L. Nico A.](#)
reference streams were captured using the pixie **profiling** system on a Silicon Graphics Mips R3000 based
elysium.cs.ucdavis.edu/~nico/publications/sisal93.ps

[Application-Controlled File Caching Policies - Cao, Felten, Li \(1994\)](#) (Correct) (52 citations)
it does not, it can keep the former in **cache** and **reduce** its **cache** miss ratio. Traditionally such caching by allowing user-level control over file **cache** replacement decisions. We use two-level **cache**
ftp.cs.princeton.edu/reports/1994/445.ps.Z

[Can High Bandwidth and Latency Justify Large Cache Blocks. - Bianchini, LeBlanc \(1994\)](#) (Correct) (1 citation)
the block size can lower the miss rate and **reduce** the number of invalidations. However, increasing
[Can High Bandwidth and Latency Justify Large Cache Blocks in Scalable Multiprocessors? Ricardo](#)
ftp.cs.rochester.edu/pub/papers/systems/94.tr486.Can_high_bandwidth_and_latency_justify_large_cache_blocks.ps.Z

[Efficient Cooperative Caching using Hints - Sarkar, Hartman \(1996\)](#) (Correct) (31 citations)
while server **caches** filter client **cache** misses to **reduce** disk accesses. A drawback of this organization is algorithms that rely on centralized control of **cache** functions, our algorithm uses hints (i.e. inexact
www.cs.arizona.edu/swarm/papers/ccache/paper.ps

[Wrong-Path Instruction Prefetching - Jim Pierce \(1994\)](#) (Correct) (16 citations)
Instruction prefetch algorithms attempt to **reduce** the performance degradation by bringing lines
University of Michigan Abstract Instruction **cache** misses can severely limit the performance of both
www.ece.orst.edu/~silu/memory/Pierce.micro29.ps

[Flaming And Thrashing: - An Examination Of](#) (Correct)
Session S3b Flaming And **Thrashing**: An Examination Of Tone In Electronic Mail
paper publishes a satirical issue and typically **profiles** a faculty member this person was not amused
fie.engrng.pitt.edu/fie2000/papers/1188.pdf

Reducing Cache Misses for CC-NUMA by Careful Page-Mapping - Jian Huang (Correct)
to the programmer, the OS and the compiler need to **reduce** average memory reference latency by minimizing
Reducing Cache Misses for CC-NUMA by Careful Page-Mapping Jian
www.cs.umn.edu/Research/Agassiz/Paper/huang.pdcs97.ps.Z

Maintaining Arc-Consistency within Dynamic Backtracking - Narendra Jussien Romuald (2000) (Correct) (2 citations)
In our experience, using a good heuristic **reduces** the number of problems on which the algorithm
more related to the fact that cbj does not avoid **thrashing** 1 than to the cost of the management of
debruyne.ifrance.com/debruyne/cp2000.pdf

Efficient Support for P-HTTP in Cluster-Based Web Servers - Aron, Druschel, Zwaenepoel (1999) (Correct) (9 citations)
the advantages of persistent connections -**reduced** server overhead and **reduced** client latency -
(LARD)a content-based policy that achieves good **cache** hit rates in addition to load balance by
that the node's main memory **cache** is already **thrashing**. Therefore, the requested content is not **cached**
www.cs.rice.edu/~aron/papers/phttp-lard.ps

An Infrastructure for Profile-Driven Dynamic Recompilation - Burger, Dybvig (1998) (Correct) (6 citations)
to support runtime reordering of basic blocks to **reduce** the number mispredicted branches and instruction
the number mispredicted branches and instruction **cache** misses, using a variant of Pettis and Hansen's
Int'l. 908-562-3966. An Infrastructure for **Profile**-Driven Dynamic Recompilation Robert G. Burger
www.cs.indiana.edu/~dyb/papers/Infrastructure.ps.gz

Ise -- An Integrated Search Environment The Manual - Lon-Chan Chu (Correct)
process will not suffer from frequent swapping or **thrashing** due to virtual memory faults. In ISE, the
line, like search strategy, search algorithm, and **profiling** status. In this report, the traveling
ISE. Section 7 describes sample runs and sample **profiles**. Finally, section 8 draws the conclusion. 2.
manip.crhc.uiuc.edu/pub/papers/PostScript/O01/O01.ps.gz

Predicting Instruction Cache Behavior - Mueller, Whalley, Harmon (1993) (Correct) (15 citations)
effect of such a memory fetch is only simulated to **reduce** bus contention, as proposed in an earlier
Predicting Instruction **Cache** Behavior Frank Mueller, David B. Whalley Marion
www.cis.famu.edu/~harmon/sigplan.ps

An Optimal Ray Traversal Scheme for Visualizing Colossal.. - Law, Yagel (1996) (Correct) (1 citation)
goal. A more immediate requirement is to **reduce** the rendering time as far as possible, so that
can simply be used advantageously to improve **cache** efficiency, for volumes that do fit in main
algorithm should be designed that minimizes this **thrashing** and optimizes the time and effort spent to
www.cis.ohio-state.edu/volviz/Papers/1996/colossal.ps.gz

Robustness in Complex Systems - Gribble (2001) (Correct) (3 citations)
were sometimes severe. 2.1 Garbage Collection **Thrashing** and Bounded Synchrony Various pieces in the
www.cs.washington.edu/homes/gribble/papers/robust.ps.gz

Cache Digests - Rousskov, Wessels (1998) (Correct) (47 citations)
this still results in a transfer of 16 Mbytes. To **reduce** the "**cache** directory" size even further, we might
Cache Digests Alex Rousskov Duane Wessels National
www-sor.inria.fr/mirrors/wcw98/31/rousskov@nlannr.net.ps

Performance Evaluation and Modeling of MPI Communications ... - Folino, Spezzano, Talia (Correct)
is greater than MPI_Bcast cost because the **reduce** operation requires a further phase of
isi-cnr.deis.unical.it:1080/~talia/hpcn98.ps

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Searching for **cacheable and non cacheable and profiling**.

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[NetCache Architecture and Deployment - Peter Danzig Network \(1998\)](#) (Correct) (7 citations)

Even if all "html" URLs were dynamic and non-**cacheable**, 80-90% of the loiting cache-control that dispatches work to a pool of slave threads. **Profiling** under high load shows that NetCache spends two No single node web cache can scale arbitrarily we **profile** and tune NetCache to run with up to 8,000 www.sor.inria.fr/mirrors/wcw98/01/NetCache-3_2.pdf

[Cache Resident Data Locality Analysis - Samdani Thornton Intel](#) (Correct)

The mechanism proposed in [9] identifies non-**cacheable** data by means of **profiling**. The scheme proposed [12]The mechanism proposed in [9] identifies non-**cacheable** data by means of **profiling**. The scheme A typical executing program has a data access **profile** that exhibits both temporal and spatial locality www.ece.msstate.edu/~mitch/pubs_dir/./ftp_dir/pubs/mascots00.ps

[Software Management of Selective and Dual Data Caches - Sánchez, González, Valero \(1997\)](#) (Correct)

The mechanism proposed in [1] identifies non-**cacheable** data by means of **profiling**. The approaches in [1] identifies non-**cacheable** data by means of **profiling**. The approaches proposed in [16] are either -based or make use of simple schemes based on **profiling**. An architecture with some similarities with ftp.ac.upc.es/pub/reports/CEPBA/1997/UPC-CEPBA-1997-4.ps.Z

[A Selective Caching Technique - John, Radhakrishnan](#) (Correct)

for specifying some portions of the memory as non-**cacheable** (though this ability is not at user level) for specifying some portions of the memory as non-**cacheable** (though this ability is not at user level) cache exclusion policy, program behavior, program **profiling**. This work was supported in part by the www.ece.utexas.edu/projects/ece/lca/ps/hpca3.ps

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Searching for **thrashing and profiling and recompilation**.

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[An Infrastructure for Profile-Driven Dynamic Recompilation - Burger, Dybvig \(1998\) \(Correct\) \(6 citations\)](#)
 Int'l. 908-562-3966. An Infrastructure for **Profile-Driven Dynamic Recompilation** Robert G. Burger
 An Infrastructure for **Profile-Driven Dynamic Recompilation** Robert G. Burger R. Kent Dybvig SAGIAN
 an efficient infrastructure for dynamic **recompilation** that can support a wide range of dynamic
www.cs.indiana.edu/~dyb/papers/Infrastructure.ps.gz

[Optimally Profiling and Tracing Programs - Ball, Larus \(1992\) \(Correct\) \(103 citations\)](#)
 Optimally **Profiling** And Tracing Programs Thomas Ball James R.
www.deas.harvard.edu/cs/academics/courses/cs248r/readings/qpt-opt-prof-trace.ps.gz

[Portable Profiling and Tracing for Parallel.. - Shende, Malony.. \(1998\) \(Correct\) \(8 citations\)](#)
 Portable **Profiling** and Tracing for Parallel, Scientific
www.cs.uoregon.edu/research/paracomp/tau/..papers/spdt98/spdt98.ps.gz

[Profile-Guided Receiver Class Prediction - Grove, Dean, Garrett, Chambers \(1995\) \(Correct\) \(46 citations\)](#)
 Appeared in OOPSLA'95, Austin, TX, October, 1995. **Profile-Guided Receiver Class Prediction** David Grove,
<ftp://cs.washington.edu/homes/chambers/oopsla95-profiles.ps.Z>

[Analysing, Profiling and Optimising Orthogonal Persistence for.. - Cutts, Hosking \(1997\) \(Correct\)](#)
 Analysing, **Profiling** and Optimising Orthogonal Persistence for Java
 must apply where necessary, either to avoid **recompilation** of vast quantities of code when new classes
www.sunlabs.com/research/forest/COM.Sun.Labs.Forest.PJava.PJW2.13_ps.ps

[DLX Simulator Directed Profiling - Jagannath \(1992\) \(Correct\)](#)
 School of Computer Science DLX Simulator Directed **Profiling** ACAPS Technical Memo 48 July 7, 1992 Sumithra
<ftp://cpsl.udel.edu/pub/doc/acaps/memos/memo48.ps.gz>

[Value Profiling and Optimization - Calder, Feller, al. \(1999\) \(Correct\) \(13 citations\)](#)
 1 (1999) 1-6 Submitted 6/98 published 3/99 Value **Profiling** and Optimization Brad Calder
www.jilp.org/vol1/v1paper2.ps

[Hardware-Based Profiling: An Effective Technique for.. - Conte, Patel, Menezes, .. \(1996\) \(Correct\) \(4 citations\)](#)
 Hardware-Based **Profiling**: An Effective Technique for **Profile-Driven**
www4.ncsu.edu/eos/users/c/conte/www/ijpp96.ps

[System Support for Automatic Profiling and Optimization - Zhang, Wang, Gloy, Chen.. \(1997\) \(Correct\) \(24 citations\)](#)
 1 System Support for Automatic **Profiling** and Optimization Xiaolan Zhang, Zheng Wang,
www.eecs.harvard.edu/morph/morph-sosp97.ps

[Salto: System for Assembly-Language Transformation.. - Rohou, Bodin.. \(1996\) \(Correct\) \(9 citations\)](#)
 on uniprocessors. Salto enables the building of **profiling**, tracing and optimization tools. The user is
<ftp://irisa.fr/techreports/1996/PI-1032.ps.gz>

[Experiences With Sun Shared Libraries in C++ - Robert Mecklenburg \(Correct\)](#)
 where we encountered our first problem. 3.1 **Profiling** and PIC Don't Mix Upon linking the application
www.cs.utah.edu/~mecklen/sun-libs/sun-shared-libs.ps

[Evaluation of A Load Profiling Approach to Routing.. - Matta, Bestavros \(1997\) \(Correct\)](#)
 Evaluation of A Load **Profiling** Approach to Routing Guaranteed Bandwidth Flows
www.cs.bu.edu/techreports/97-013-route-profiling-evaluation.ps.Z

[Mechanisms and Interfaces for Software-Extended Coherent Shared.. - Chaiken \(1994\) \(Correct\) \(3 citations\)](#)
 forward progress in the face of protocol **thrashing** scenarios and high-availability interrupts